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L1	172	703/14.ccls. and @pd>"20050601"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2006/04/03 15:04
L2	21	((FIFO adj (queue or memory)) same (instrumentation or statistic\$1)) and @ad<"20011211"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2006/04/03 15:11
L3	229	((FIFO) same (instrumentation or statistic\$1)) and @ad<"20011211"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2006/04/03 16:05
L4	8	((tri-state) same (instrumentation or statistic\$1)) and @ad<"20011211"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2006/04/03 16:06
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29 Dec. 1995  
[AbstractPlus](#) | Full Text: [PDF](#)(1724 KB) IEEE STD
- ☐ 3. **Hardware/software codesign and rapid prototyping of embedded systems**  
Slomka, F.; Dorfel, M.; Munzenberger, R.; Hofmann, R.;  
[Design & Test of Computers, IEEE](#)  
Volume 17, Issue 2, April-June 2000 Page(s):28 - 38  
Digital Object Identifier 10.1109/54.844331  
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- ☐ 4. **Using VHDL for high-level, mixed-mode system simulation**  
Srivastava, M.B.; Brodersen, R.W.;  
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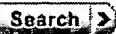
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Plaza, P.; Merayo, L.A.; Diaz, J.C.; Conesa, J.L.;  
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Volume 4, Issue 3, Sept. 1996 Page(s):405 - 416  
Digital Object Identifier 10.1109/92.532040  
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Riesco, J.; Diaz, J.C.; Merayo, L.A.; Conesa, J.L.; Santos, C.; Juarez, E.;  
[European Design and Test Conference, 1997. ED&TC 97. Proceedings](#)  
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Diaz, J.C.; Plaza, P.; Crespo, J.;  
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Digital Object Identifier 10.1109/DATE.1998.655842  
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- ☐ 5. **Warren: a low-cost ATM home area network**  
Greaves, D.J.; Bradbury, R.J.;  
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Volume 4, Issue 3, Sept. 1996 Page(s):405 - 416  
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- ☐ 2. **Energy-effective issue logic**  
Folegnani, D.; Gonzalez, A.;  
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30 June-4 July 2001 Page(s):230 - 239  
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- ☐ 3. **On the way to the 2.5 Gbits/s ATM network ATM multiplexer demultiplexer ASIC**  
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[Design, Automation and Test in Europe, 1998., Proceedings](#)  
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[Global Telecommunications Conference, 1998. GLOBECOM 98. The Bridge to Global](#)  
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- ☐ 6. **Modeling and simulation of a readout architecture for pixel detectors**  
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- ☐ **7. Different queuing policies for handover requests in low Earth orbit mobile satellite systems**  
Del Re, E.; Fantacci, R.; Giambene, G.;  
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Volume 48, Issue 2, March 1999 Page(s):448 - 458  
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- ☐ **11. A class of end-to-end congestion control algorithms for the Internet**  
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- ☐ **12. An approximation for performance evaluation of stationary single server queues**  
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- ☐ **16. Performance evaluation of client-server systems**  
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- ☐ **17. Handoff ordering using signal prediction priority queuing in personal communication systems**  
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- ☐ **18. Bandwidth Allocation Strategies in Wide-Band Integrated Networks**  
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- ☐ **20. A performance model for ATM switches with general packet length distributions**  
Xi-Ren Cao; Towsley, D.;  
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Cozzani, I.; Giordano, S.;  
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Rao, D.M.; Thondugulam, N.V.; Radhakrishnan, R.; Wilsey, P.A.;

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**24. Design Tradeoffs for Local Access Systems in Computer Networks**

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**25. Performance models of statistical multiplexing in packet video communications**

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Volume 2, 31 Jan-4 Feb 1999 Page(s):1287 - 1292 vol.2  
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- ☐ 3. **Adaptive impulse detection using center-weighted median filters**  
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Li Chen; Dey, S.; Sanchez, P.; Sekar, K.; Ying Chen;  
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- ☐ **7. A comparative study of usage-based charging schemes**  
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- ☐ **8. Subject index**  
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[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on \[see also](#)  
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